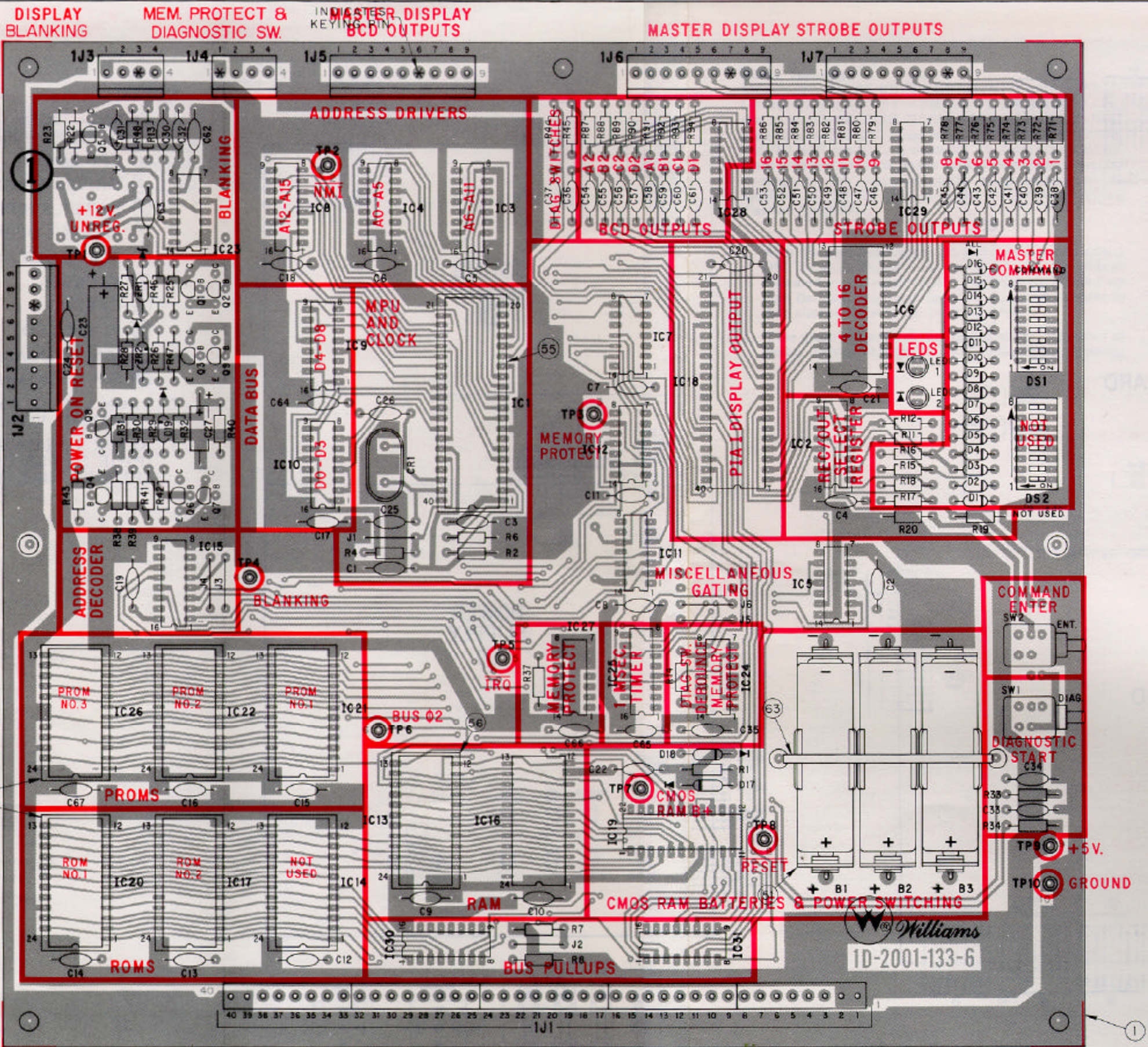
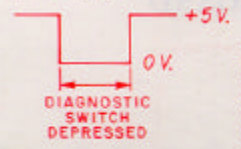


REVISION LETTER	REVISION
D	REVISED AND REDRAWN REVISED DWG. TO CONFORM TO ARTWORK NO. 18-2001-133 LATEST ISSUE NO. 6. R. GAY, 1-8-79
E	ITEM NO. 3, MFG'S PT. NO. WAS IN5991. E.C.O. 4670 R.GAY, 1-31-79

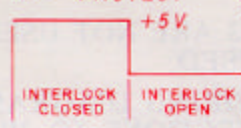


TP1 +12V. UNREG

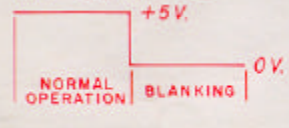
TP2 NMI



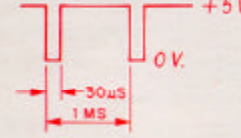
TP3 MEMORY PROTECT



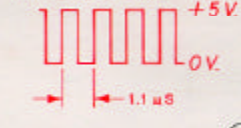
TP4 BLANKING



TP5 IRQ



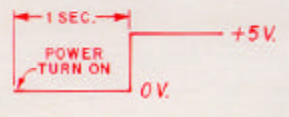
TP6 BUS Φ2



TP7 CMOS RAM B+

POWER ON 4.3V.
POWER OFF 3.9V.

TP8 RESET



TP9 +5V.

TP10 GND

LOGIC POWER
BUS INPUT

INTERBOARD CONNECTOR

Williams
1D-2001-133-6